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		Filing Date			
		First Named Inventor	Benoit NADEAU-DOSTIE		
		Group Art Unit			
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	LVPAT067US

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		NAKAHARA et al, "Built-In Self-Test for GHz embedded SRAMS Using Flexible Pattern Generator and New Repair Algorithm", 1999 Proceedings of the International Test Conference, October, 1999, p. 301.	
		McCONNELL et al., "Test Repair of Large Embedded DRAMs: Part 1", Proceedings International Test Conference 2001, October 30 - November 01, 2001 Baltimore, Maryland, p. 163	
		SCHOBBER, et al., "Memory Built-In Self-Repair", 2001 Proceedings of the International Trade Conference, October 30 - November 01, 2001, p. 995	
		KWON et al., "Linear Search Algorithm for Repair Analysis with 4 Spare Row/4 Spare Column", Proceedings of the 2nd IEEE Asia-Pacific Conference on ASIC, Cheju Island, Korea, August 28th, 2000, paper 15.1.	

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